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Gernot Koch, Udo Kebschull, Wolfgang Rosenstiel
September 1994 **Proceedings of the 3rd international workshop on Hardware/software co-design**

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We present a prototyping environment with special benefit for hardware/software codesign which we use as target architecture in the COBRA project. This architecture is very flexible, easy extensible, and provides a high gate complexity. It supports standard processor integration as well as processor emulation.

2 ICEBERG: an embedded in-circuit emulator synthesizer for microcontrollers
Ing-Jer Huang, Tai-An Lu
June 1999 **Proceedings of the 36th ACM/IEEE conference on Design automation**

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Frank Vahid, Tony Givargis
March 1999 **Proceedings of the seventh international workshop on Hardware/software codesign**

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Keywords: IP, cores, methodology, system-on-a-chip

4 Fast development of source-level debugging system using hardware emulation (short paper)
Sang-Joon Nam, Jun-Hee Lee, Byoung-Woon Kim, Yeon-Ho Im, Young-Su Kwon, Kyong-Gu Kang, Chong-Min Kyung
January 2000 **Proceedings of the 2000 conference on Asia South Pacific design automation**

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Kurt Keutzer
February 1997 **Proceedings of the 1997 ACM fifth international symposium on Field-**

programmable gate arrays

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M. Meerwein, C. Baumgartner, W. Glauert
May 2000   **Proceedings of the eighth international workshop on Hardware/software codesign**
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Sungjoo Yoo, Jong-Eun Lee, Jinyong Jung, Kyungseok Rha, Youngchul Cho, Kiyoung Choi
January 2000 **Proceedings of the conference on Design, automation and test in Europe**
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Frank Vahid
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9 A Hardware/Software Concurrent Design for a Real-Time SP@ML MPEG2 Video-Encoder Chip Set
Mitsuo Ikeda, Tsuneo Okubo, Tetsuya Abe, Yoshinori Ito, Yutaka Tashiro, Ryota Kasai
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